

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S155	927	(714/30).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/01 14:25
S156	515	(714/37).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/01 14:26
S160	3	S158 and S159	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/01 14:27
S159	1415	S155 or S156	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/01 14:27
S157	0	(compil\$5 with ilde).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/01 14:27
S158	90	(compil\$5 with diagnos\$3).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/01 14:28

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L1	941	(714/30).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/27 18:43
L2	519	(714/37).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/27 18:43
L3	1433	L1 or L2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/27 18:43
L4	1090	L3 and (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/27 18:43
L5	6170	redundant with (processor or CPU)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/27 18:43
L6	35	L5 and L4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/27 18:43

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This optimization is **opportunistic** in the sense that the **compiler** cannot have a perfect knowledge of the state of the RSE when it inserts the extra alloc ...

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implemented using **compiler** directives in an **opportunistic** manner. ... out as a **test** to see if the approach produced meaningful results. Therefore speed of ...

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GNU environment, depending on their own preferences. Two subjects in each **testing** group had problems in setting up the GNU **compiler** to find the correct ...

ieeexplore.ieee.org/iel5/8855/27999/01251026.pdf?arnumber=1251026

[PDF] The first IA-64 microprocessor - Solid-State Circuits, IEEE Journal of

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and speculation work together to give the **compiler** a significant ... High-speed blocks are implemented using **opportunistic** time ...

ieeexplore.ieee.org/iel5/4/19075/00881197.pdf

[PDF] WP 25.7 The First IA-64 Microprocessor: A Design for Highly ...

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and store has a 2b cache hint field in which the **compiler** encodes its ... High-speed blocks use **opportunistic** time borrowing (OTB) domino circuits [1]. ...

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[PDF] AI Planner Assisted Test Generation

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B. **Opportunistic** sequences. (1) Random selection. The length of the sequences ...

An automatic generator for **compiler testing**, IEEE Transactions on ...

www.springerlink.com/index/U811070140755874.pdf

[PDF] Text planning with opportunistic control

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generation and obtain experimental results from **testing** the planner. ... ization stage is handled by the GenKit generation grammar **compiler** [73] ...

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Glasgow) lack JIT **compiler** support for this very reason. – Application-specific optimizations: One could be more **opportunistic** ...

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on **opportunistic** peephole-style optimization. The GCC **compiler** also takes ... is reduced to **testing** whether the head and tail vertices of e are common to a ...

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the developers produce code to pass these **test** cases. The process. is essentially "**opportunistic**" in nature [11]. A developer writes a ...

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